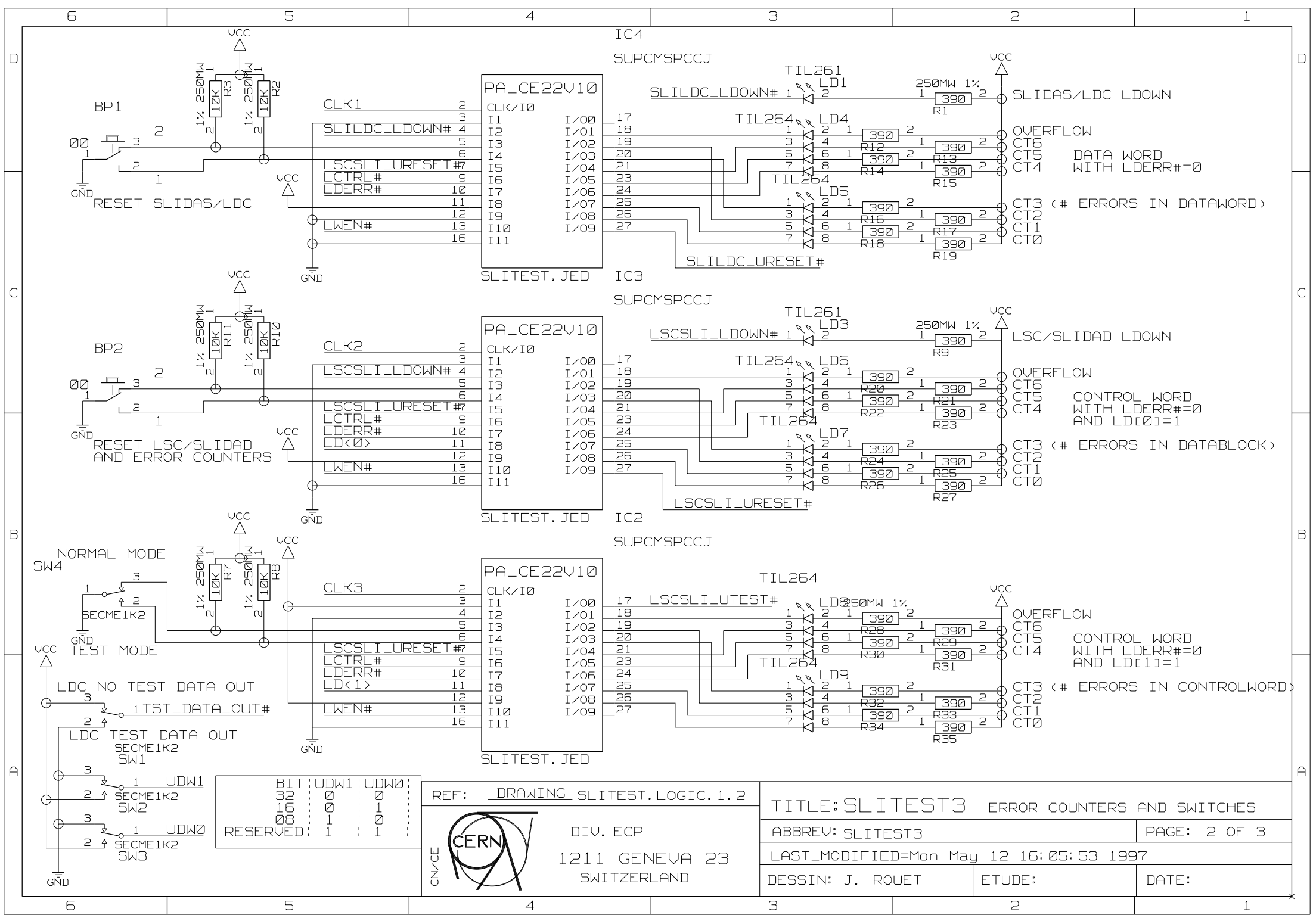

BIT	UDW1	UDW0
32	0	0
16	0	1
08	1	0
RESERVED	1	1

REF: DRAWING SLITEST.LOGIC.1.2

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TITLE: SLITEST3 ERROR COUNTERS AND SWITCHES

ABBREV: SLITEST3 | PAGE: 2 OF 3

LAST_MODIFIED=Mon May 12 16:05:53 1997

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