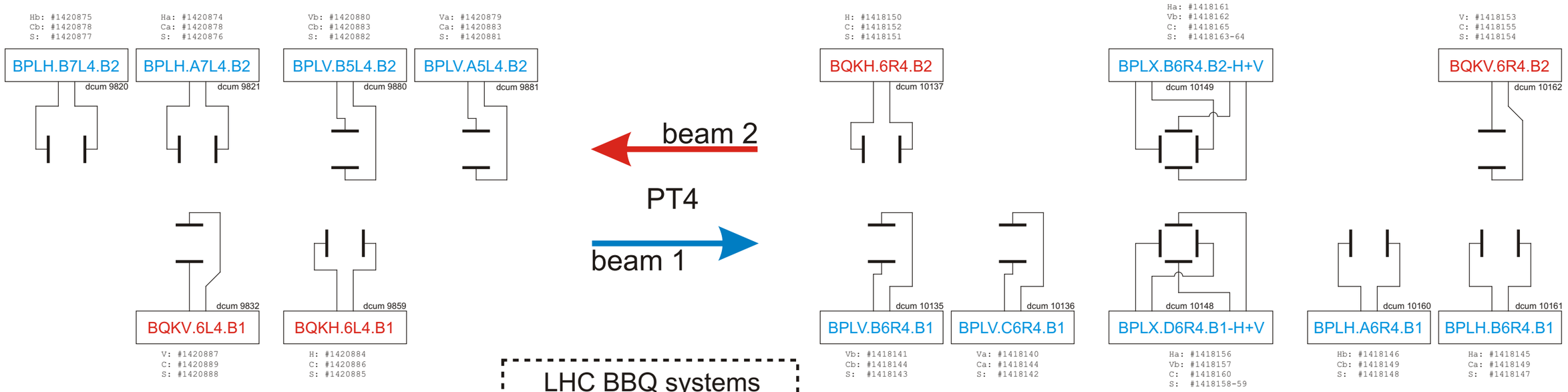

LHC BBQ systems

Diagram v. 7/07/08, M. Gasior, AB-BI-QP

Legend:

2c	- 2 Coaxial cables
m48	- 48-wire multiwire cable
H (V)	- Horizontal (Vertical) signal
Ha (Hb)	- H signal on digitizer channel A (B)
C	- Control
S	- Spare
#xxx	- cable number xxx

Color coding:

pick-ups (BPL)
kickers (BQK)
BY07-BY12
tunnel-surface
UA43-UA47
damper

BY09.UA43

BQKH.6L4.B1 (2c+m26)
H: #1420884
C: #1420886
S: #1420885

BQKV.6L4.B1 (2c+m26)
V: #1420887
C: #1420889
S: #1420888

BY11.SX4 (2c+m48)
- not used
S: #1420959-60
C: #1420961

damper tunnel UX45 (2c)
- not used
AYADT24.UX45: #1420952
AYADT19.UX45: #1420953

BY10.UA43

BPLH.A7L4.B2 (2c+m26)
BPLH.B7L4.B2 (2c)
Ha: #1420874
Hb: #1420875
C: #1420878
S: #1420876-77

BPLV.A5L4.B2 (2c+m26)
BPLV.B5L4.B2 (2c)
Va: #1420879
Vb: #1420880
C: #1420883
S: #1420881-82

BY11.SX4 (4c+m48)
- AFE chBs (direct)
BPLH.B7L4.B2 #1420954
BPLV.B5L4.B2 #1420955
- copies of chAs (buffer amp)
BPLH.A7L4.B2 #1420956
BPLV.B5L4.B2 #1420957
- control of chBs (slave)
pin 1-26/48 of #1420958

BY06.UA47 (4c+m48)
- PLL DACs/tickler signals
B1-H: #1420892 (R->L)
B1-V: #1420893 (R->L)
B2-H: #1420894 (L->R)
B2-V: #1420895 (L->R)
- not used
C: #1420896

FFT1.B2 ("continuous")
slot #15, codec slave, AFE control
PLL.B2 ("PLL system")
slot #17, codec master, BQK control
CFV-UA43-BQPLL

BY10.SX4

BY12.UA47 (4c+m48)
- copies (buffer amp)
B1-H: #1418187
B1-V: #1418188
B2-H: #1418189
B2-V: #1418190
- control m48
not used #1418191

damper surface SR4 (8c)
- not used
S: AYADT44.SR4: #1418204-11

DEV.B1 ("development")
slot #4, codec master, gain control
DEV.B2 ("development")
slot #15, codec master, gain control
CFV-SX4-BQ

BY11.SX4

BY09.UA43 (4c+m48)
- AFE chBs (direct)
BPLH.B7L4.B2 #1420954
BPLV.B5L4.B2 #1420955
- copies of chAs (buffer amp)
BPLH.A7L4.B2 #1420956
BPLV.B5L4.B2 #1420957
- control of chBs (slave)
pin 1-26/48 of #1420958

BY07.UA47 (4c+m48)
- AFE chBs (direct)
BPLH.A6R4.B1 #1418182
BPLV.B6R4.B1 #1418183
- copies of chAs (buffer amp)
BPLH.B6R4.B1 #1418184
BPLV.C6R4.B1 #1418185
- control of chBs (slave)
pin 1-26/48 of #1418186

BY10.UA43 (2c+m48)
- not used
S: #1420959-60
C: #1420961

BY06.UA47 (2c+m26)
- not used
S: #1418192-93
C: #1418194

BY06.UA47

BQKH.6R4.B2 (2c+m26)
H: #1418150
C: #1418152
S: #1418151

BQKV.6R4.B2 (2c+m26)
V: #1418153
C: #1418155
S: #1418154

BY11.SX4 (2c+m48)
- not used
S: #1418192-93
C: #1418194

BY10.UA43 (4c+m48)
- PLL DACs/tickler signals
B1-H: #1420892 (R->L)
B1-V: #1420893 (R->L)
B2-H: #1420894 (L->R)
B2-V: #1420895 (L->R)
- not used
C: #1420896

BY07.UA47

BPLH.A6R4.B1 (2c+m26)
BPLH.B6R4.B1 (2c)
Hb: #1418146
Ha: #1418145
C: #1418149
S: #1418147-48

BPLV.B6R4.B1 (2c+m26)
BPLV.C6R4.B1 (2c)
Vb: #1418141
Va: #1418140
C: #1418144
S: #1418142-43

BY12.UA47 (8c)
- PLLs to damper in BY12
B1-H: 1#1424268
B1-V: 2#1424269
B2-H: 5#1424272
B2-V: 6#1424273
- not used
3-4#1424270-1,7-8#1424274-5

BY11.SX4 (4c+m48)
- AFE chBs (direct)
BPLH.A6R4.B1 #1418182
BPLV.B6R4.B1 #1418183
- copies of chAs (buffer amp)
BPLH.B6R4.B1 #1418184
BPLV.C6R4.B1 #1418185
- control of chBs (slave)
pin 1-26/48 of #1418186

damper tunnel UX45 (2c)
- not used
AYADT02.UX45: #1418176
AYADT08.UX45: #1418177

FFT1.B1 ("continuous")
slot #4, codec slave, AFE control
PLL.B1 ("PLL system")
slot #6, codec master, BQK control
CFV-UA47-BQPLL

BY12.UA47

BPLX.D6R4.B1-H (2c+m26)
BPLX.D6R4.B1-V (2c)
Ha: #1418156
Vb: #1418157
C: #1418160
S: #1418158-59

BPLX.B6R4.B2-H (2c+m26)
BPLX.B6R4.B2-V (2c)
Ha: #1418161
Vb: #1418162
C: #1418165
S: #1418163-64

BY07.UA47 (8c)
- PLLs from by07 to damper
B1-H: 1#1424268
B1-V: 2#1424269
B2-H: 5#1424272
B2-V: 6#1424273
- not used
3-4#1424270-1,7-8#1424274-5

BY10.SX4 (4c+m48)
- copies (buffer amp)
B1-H: #1418187
B1-V: #1418188
B2-H: #1418189
B2-V: #1418190
- control m48
not used #1418191

damper tunnel UX45 (4c)
- DAC outputs to the damper
B1-H: #1418178 (AYADT02.UX45)
B1-V: #1418179 (AYADT08.UX45)
B2-H: #1418180 (AYADT25.UX45)
B2-V: #1418181 (AYADT19.UX45)

FFT2.B1 ("on demand")
slot #4, codec master, gain control
FFT2.B1 ("on demand")
slot #15, codec master, gain control
CFV-UA47-BQ