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AtlasSiliconRodGroup

- Set GROUP = HeatherGray, JohnHill, JohnJoseph, AndreasKorn, TrevorVickey, JedBiesiada, DaveRobinson, ShihChiehHsu, MarkCooke
- Set ALLOWTOPICCHANGE = AtlasSiliconRodGroup
- New DSP Development Group: PixelDsp

ATLAS Silicon ROD

Hypernews Forum

- hn-atlas-SiliconROD@cernNOSPAMPLEASE.ch

Board Status and Service Log

- BoardStatusAndServiceLog
- get_data: script to download ROD board log data for processing

DSP Software

Currently installed version (31.10.2022):

- 1.5.7b
- AtlasSiliconRodDspCode
- spru328b.pdf: CCS User Guide (includes discussion on .gel files)
- spru190d.pdf: TI Peripherals Reference Guide (describes on-chip peripherals for the DSPs)

FPGA Firmware (current versions as of Feb 9th, 2010)

Currently installed versions (31.10.2022):

- efb: f49
- fmt: 60f55
- rcf: f54
- rtr: f32
- John Joseph's LBNL Silicon-ROD ftp webpage [🔗](#)
- Location of the current FPGA binary files and revision history for the SiROD Electronics Board [🔗](#)
- Pixel Formatter (Revision History [🔗](#)): fmt_p3F-1f.bin
- SCT Formatter (Revision History [🔗](#)): fmt_s38f.bin
- Event Fragment Builder (Revision History [🔗](#)): efb_v31f .bin
- Router (Revision History [🔗](#)): rtr_v2Df.bin
- Controller (Revision History [🔗](#)): rcf_v37f.bin
- Program Reset Manager: prm_v17f.exo (PROM update by JTAG or PROM programmer only)

Manuals and Appendicies

- ROD Manual (Revised May 28th, 2009): [usersManual-v164.pdf🔗](#)
- ROD Manual Appendix A (Revised June 2nd, 2010): [operationsManual_appA-v22.pdf🔗](#)
- ROD Manual Appendix B (Revised March 11, 2008): [operationsManual_appB-v11.pdf🔗](#)

Design Documents

- ROD DSP SIMM REV-B Assembly Drawing: SiROD-dspSimm-RevB-AssemblyDwg.pdf
- ROD DSP SIMM REV-B Bill of materials: SiROD-dspSimm-RevB-BOM.pdf
- ROD DSP SIMM REV-B Fabrication Drawing: SiROD-dspSimm-RevB-FabricationDwg.pdf
- ROD DSP SIMM REV-B Schematic: SiROD-dspSimm-RevB-Schematic.pdf
- ROD REV-F Assembly Drawing: SiROD-RevF-AssemblyDwg.pdf
- ROD REV-F Bill of Materials: SiROD-RevF-BOM.pdf
- ROD REV-F Fabrication Drawing: SiROD-RevF-FabricationDwg.pdf
- ROD REV-F Schematic: SiROD-RevF-Schematic.pdf

Module Links and Documents

PXL Specific

- ATLAS PXL Webpage [↗](#)
- PXL Wiki Webpage

SCT Specific

- ATLAS SCT Webpage [↗](#)
- SCT Wiki Webpage
- SctRodDaq Wiki Webpage [↗](#)
- ABCD Specs (v1.2): abcd3t_spec_v1.2.pdf

SBC Links and Documents

- ATLAS RCC SBC Support [↗](#)
- SctSbcBoardStatusAndServiceLog

BOC Links and Documents

- BOC homepage [↗](#)
- Spreadsheet of Tx plugin swaps in 2009:
<https://twiki.cern.ch/twiki/pub/Main/AtlasSiliconRodGroup/Tx-replacement.xls>
- SctBocBoardStatusAndServiceLog

TIM Links and Documents

- TIM homepage [↗](#)
- TIM Module Reference Manual [↗](#)
- TimBoardStatusAndServiceLog

ROD Crate Links and Documents

- ATLAS Crate information [↗](#)
- ESS web page - top right for link to crate database [↗](#)
- SctRodCrateStatusAndServiceLog

Teststand Documents

Talks, Tutorials and Presentations

- **How to Set up, Compile and Develop DSP code in SR1:** Tom Barber, Novemeber 2008:
SctDSPTutorial (under construction)
- **Progress on the Slave DSP Code,** H. Gray, Pixel Systemtest Presentation (February 7, 2007):
hgray_sdsp_status_070207.pdf
- **Status of the New DSP Code,** A. Korn, Pixel Systemtest Presentation (February 7, 2007):
akorn_dsp_status_070207.pdf
- **A Read-Out Driver for Silicon Detectors in ATLAS,** T. Vickey, 12th LECC Workshop
Proceedings (September 26, 2006): tvickey_LECC_2006_proceedings.pdf
- **A Read-Out Driver for Silicon Detectors in ATLAS,** T. Vickey, 12th LECC Workshop (September
26, 2006): tvickey_LECC_2006.pdf tvickey_LECC_2006.ppt
- **Pixel ReadOut/ROD hardware Overview,** A. Korn, Seminar Wuppertal (June 1, 2006):
PixelReadOutTalk3.pdf
- **Status of DSP Code,** A. Korn, et al., ID-Week Presentation (September 28, 2005):
pixelweek_dsp_050928.pdf
- **SCT ROD DSP Code Developments,** T. Vickey and P. W. Phillips, ID-Week Presentation
(September 26, 2005): tvickey_idweek_092605.pdf
- **Status of DSP Code,** A. Korn, et al., Pixel-Week Presentation (June 20, 2005):
pixelweek_dsp_050620.pdf
- **ROD DSP Software,** D. Ferguson, SCT/Pixel DAQ Workshop (Date Unknown):
Doug_DAQ_Workshop.pdf
- **ROD DSP Software Infrastructure and common primitives,** D. Ferguson FDR/PRR of the pixel
ROD Talk 1 (Oct. 27, 2003): DPSF_10272003_1.pdf
- **Pixel ROD Timing Performance,** D. Ferguson FDR/PRR of the pixel ROD Talk 2 (Oct. 27, 2003):
DPSF_10272003_2.pdf
- **ROD DSP software,** D. Ferguson SCT back-end electronics FDR/PRR (May 13, 2003):
DPSF_05132003.pdf
- **Pixel and SCT ROD Running, Calibration, Monitoring and Error Handling,** D. Fasching, et al.,
Second ATLAS ROD Workshop (October 4, 2000): fasching_ROD_hardware.ps

Contact Information

This topic: Main > AtlasSiliconRodGroup

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