

19/11/2010

Registration for the Intel S/W products workshop (29th-30th November) is now open!

Inside CERN openlab III collaboration, Intel is proposing a two-day workshop for CERN's community specifically dedicated to new Intel s/w products that will be soon available to the market (launch planned in November). This session will provide CERN's software developers with deep insight into new features and tools helping the community to leverage on today's and future CPU micro-architectures.

Hans Pabst and Levent Akyil (Intel SSG), will lead these two days addressing Array Building Block ArBB (aka Ct), and VTune™ Amplifier XE 2011. We are also planning hands-on exercises using real code samples from the LHC applications. The workshop will take place on 29th-30th November 2010, in the Kjell Johnsen Auditorium (Bld 30-7-018).

For more information about the preliminary agenda, please check the CERN openlab website (events section).

The workshop is open to interested people in IT (as well as to physicists). Please confirm your presence and /or extend this invitation to colleagues who will be willing to participate. Everybody should plan to bring a laptop for accessing the openlab servers during the exercises.

If you are interested in attending this workshop please mail your reply to Sverre Jarp: [Sverre\(dot\)Jarp\(at\)cern\(dot\)ch](mailto:Sverre(dot)Jarp(at)cern(dot)ch).